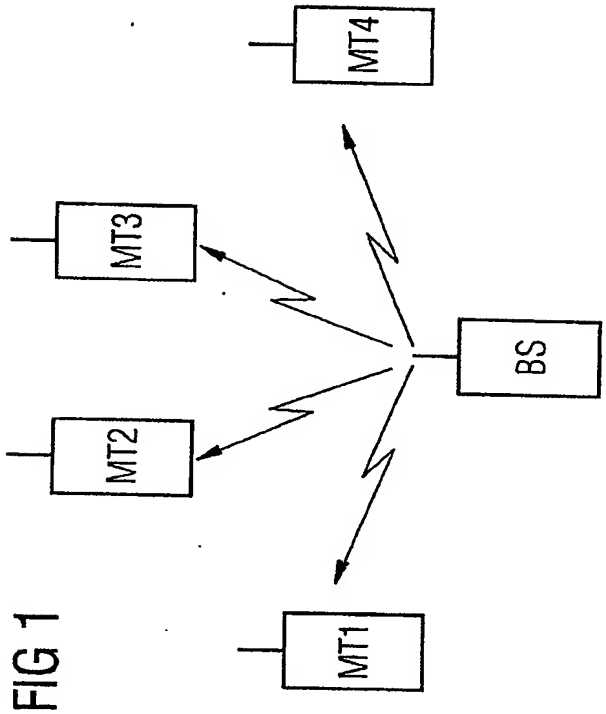
MT4
MT3
MT2
MT1
BS

FIG 1

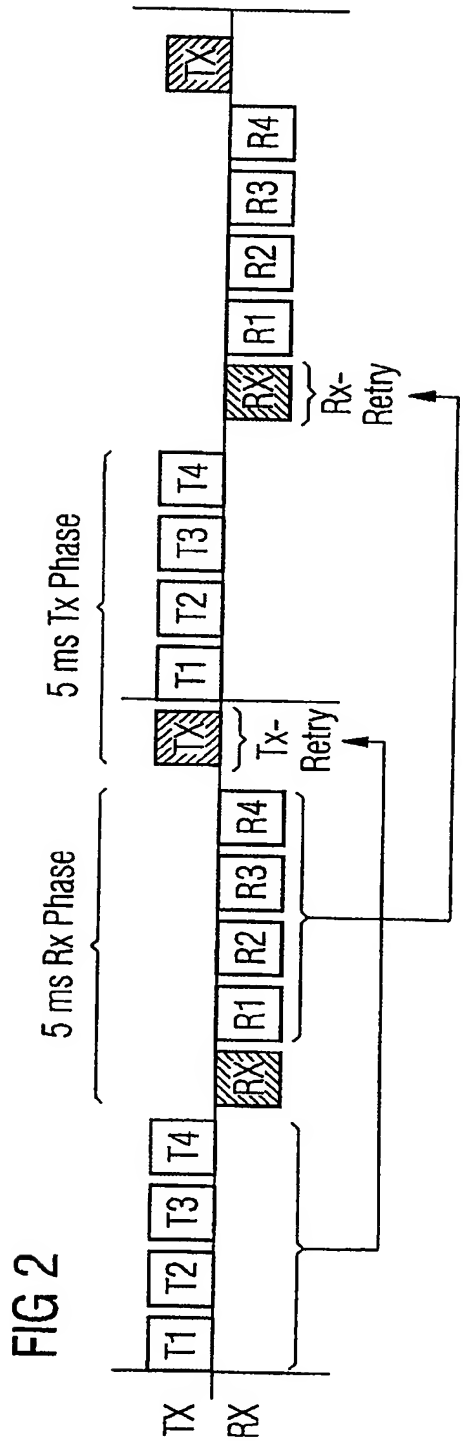
TX
R4
R3
R2
R1
RX
Rx-
Retry
5 ms Tx Phase
T4
T3
T2
T1
TX
Tx-
Retry
5 ms Rx Phase
R4
R3
R2
R1
RX
T4
T3
T2
T1
TX
RX

FIG 2